

一级代理商：

深圳市弗瑞鑫电子有限公司

地址：深圳市宝安区西乡大道302号金源商务大厦B座三楼

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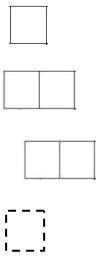
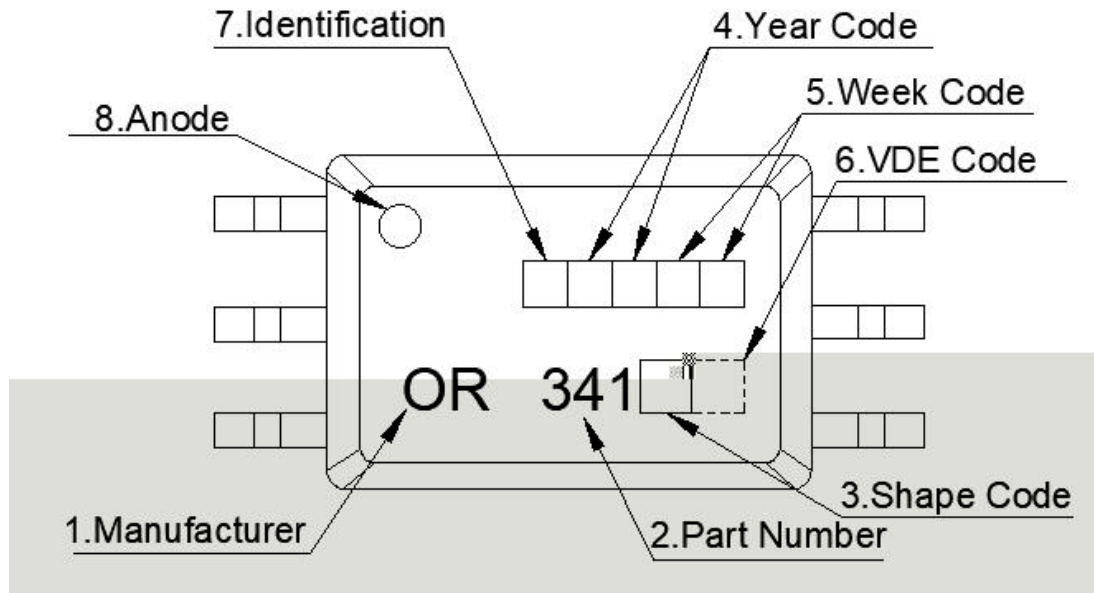
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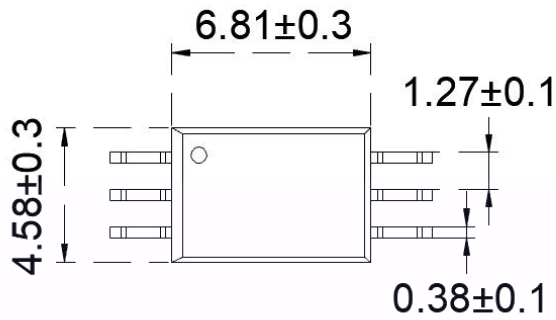
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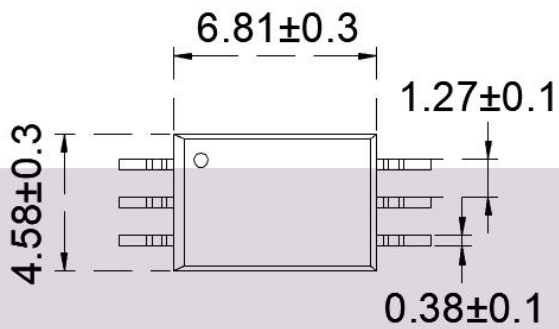
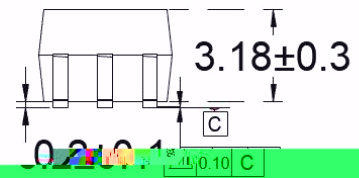
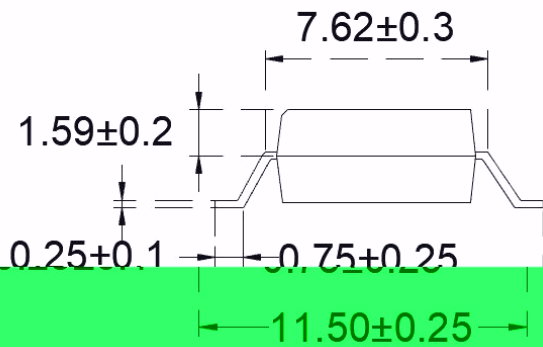
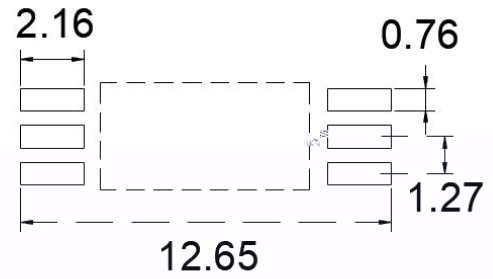
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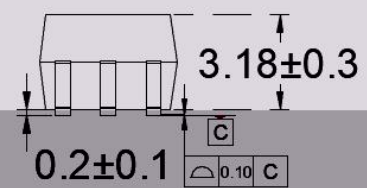
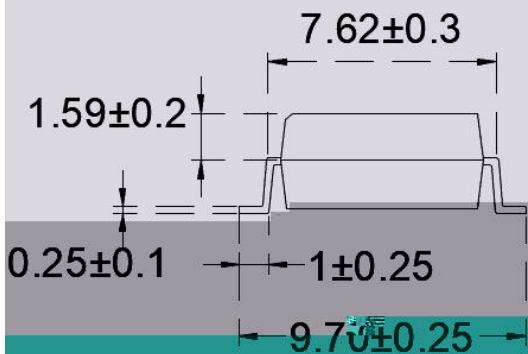
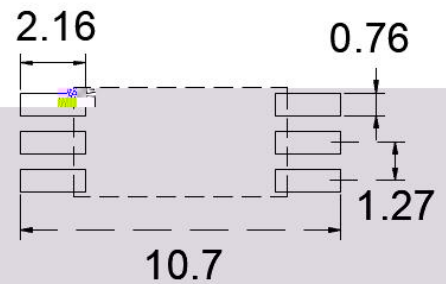
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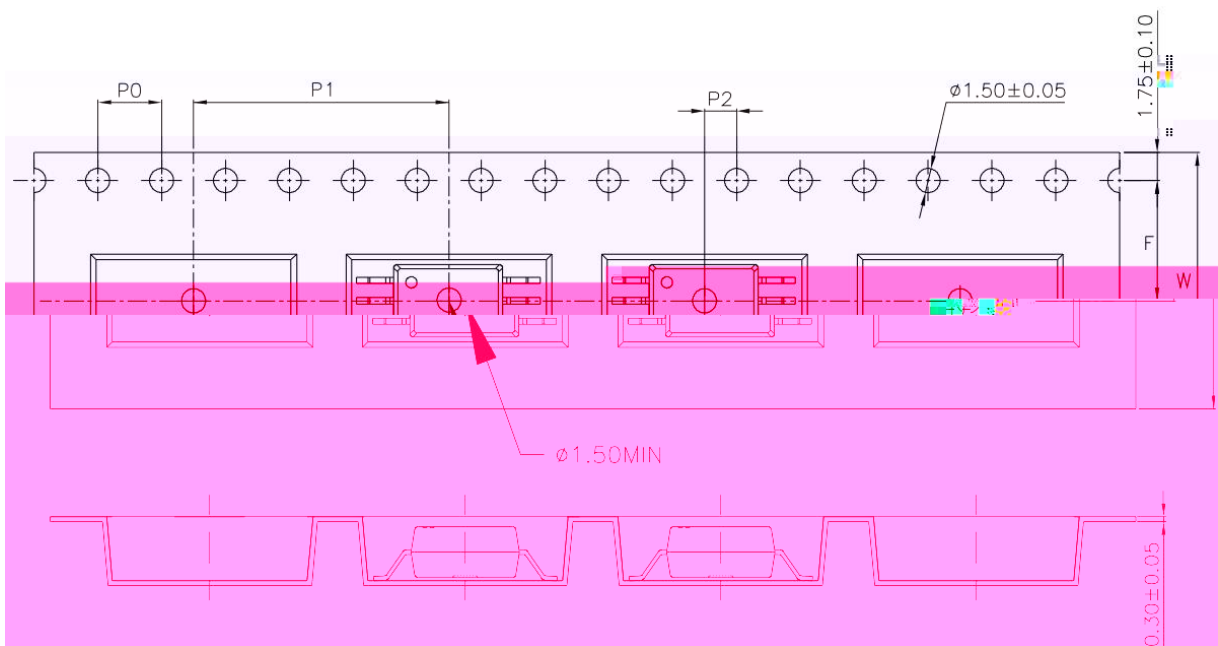
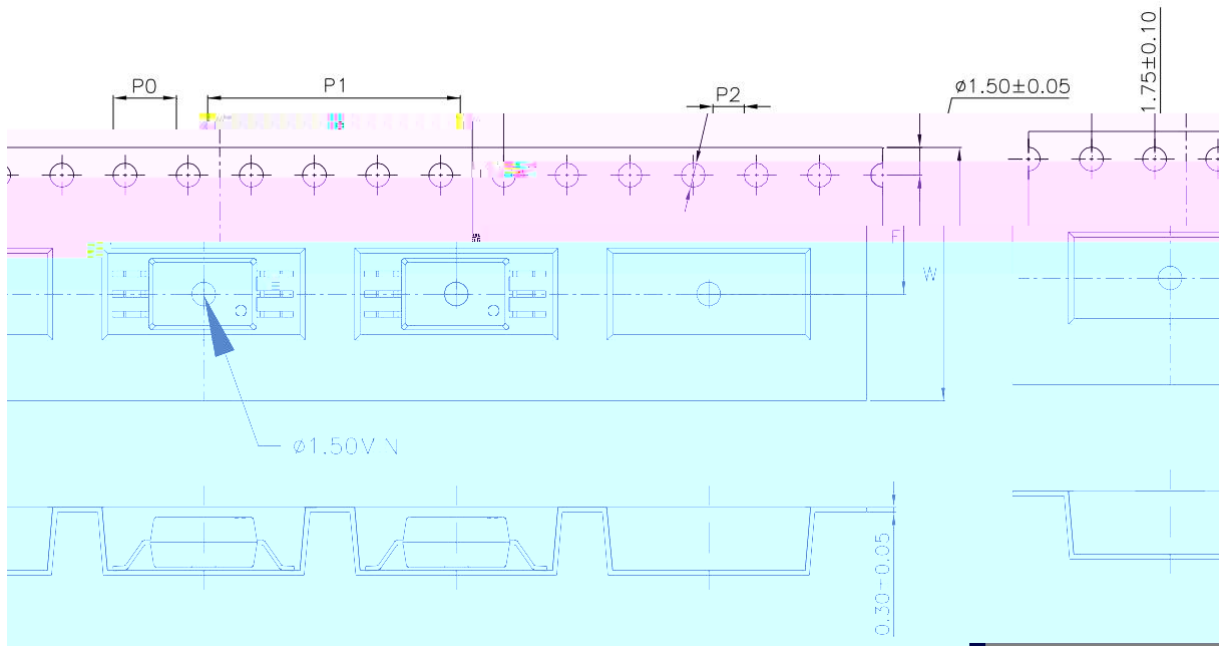
Land Pattern Recommendation

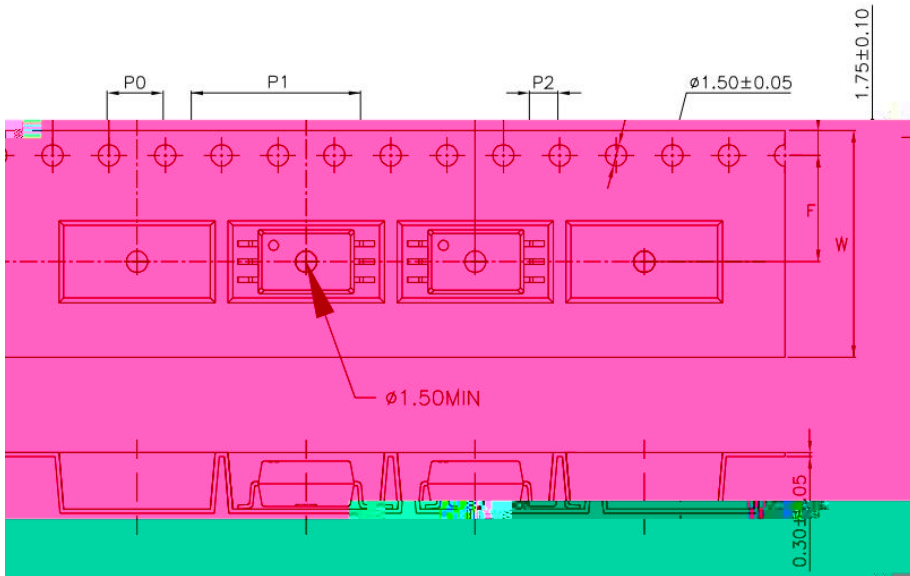
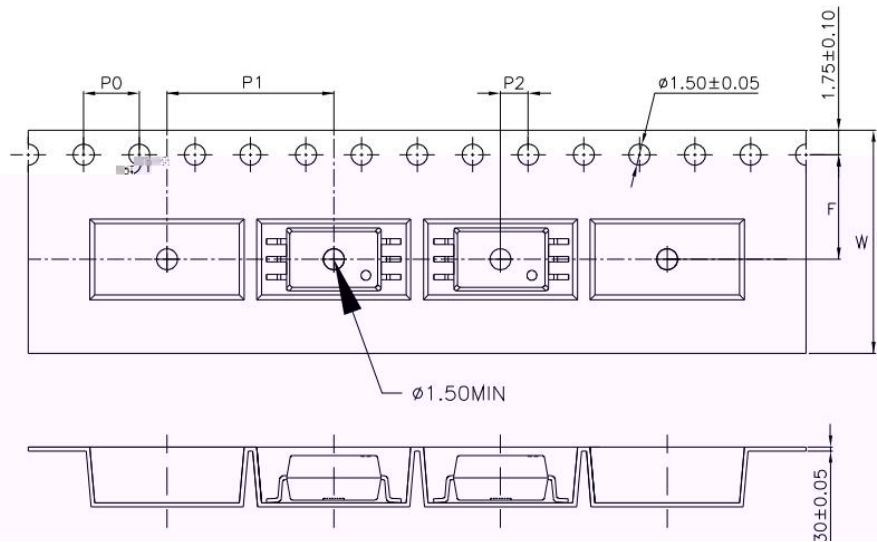


Land Pattern Recommendation



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Shenzhen Orient Components Co.,LTD




Material Code: J20PCXXXXXX

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P/N: OR-XXXXXX

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Lot No.: XXXXXX-XXXX-TX-X

|||||

D/C: XXXX

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Qty: XXXX PCS

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内箱码

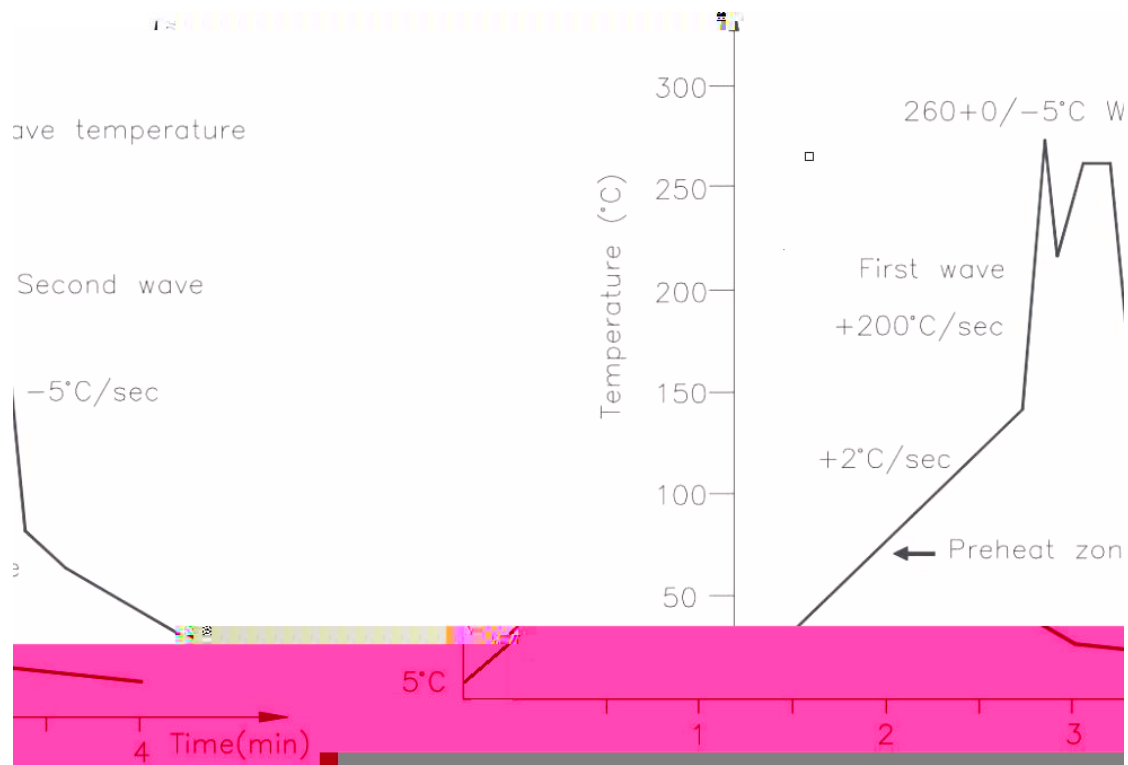
外箱码

“XXXXXXXXXXXXXXXXXX” (一体机序列码)

Made in China



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Temperature	$380+0/-5^{\circ}\text{C}$
Time	3 sec max

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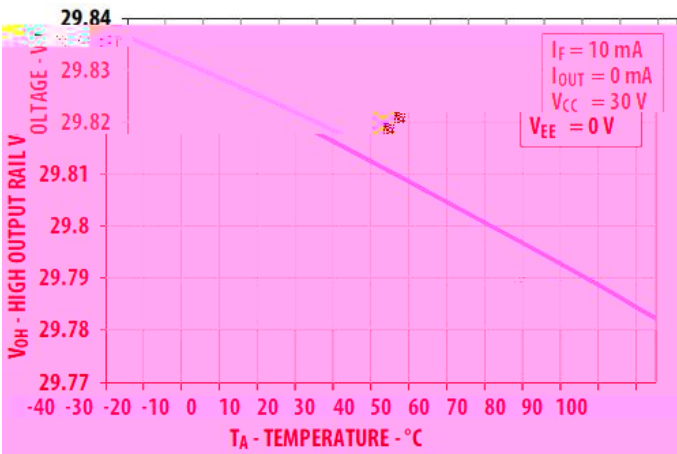


Figure 1. High output rail voltage vs. temperature

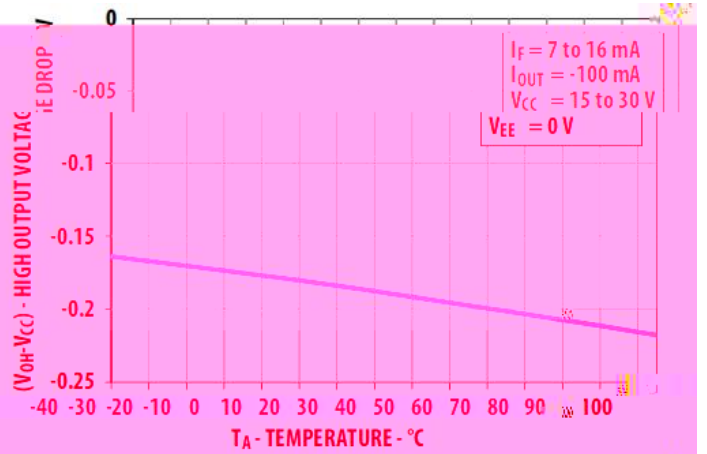


Figure 2. V_{OH} vs. temperature

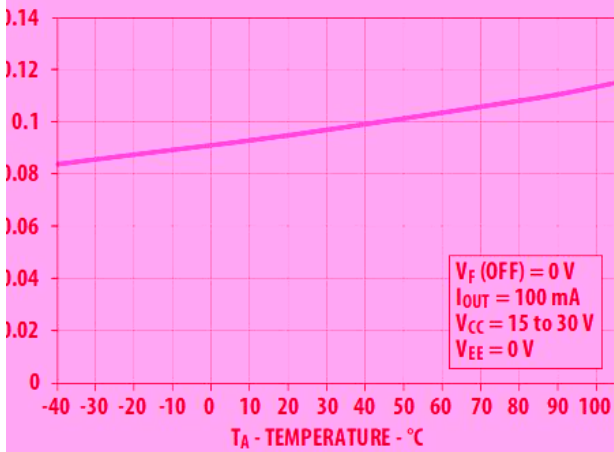


Figure 3. V_{OL} vs. temperature

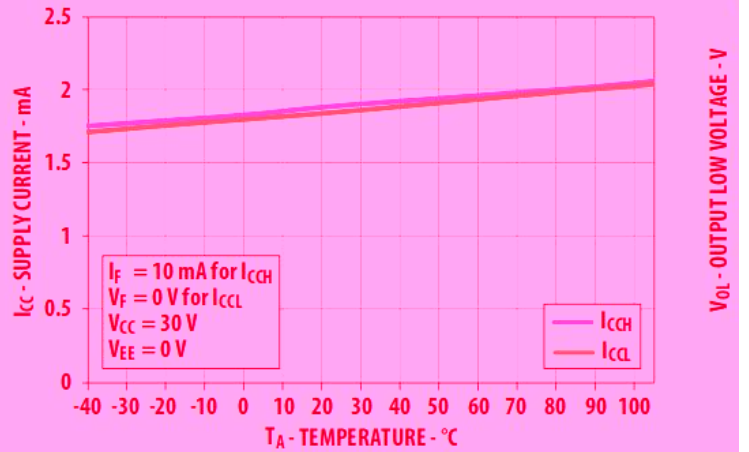
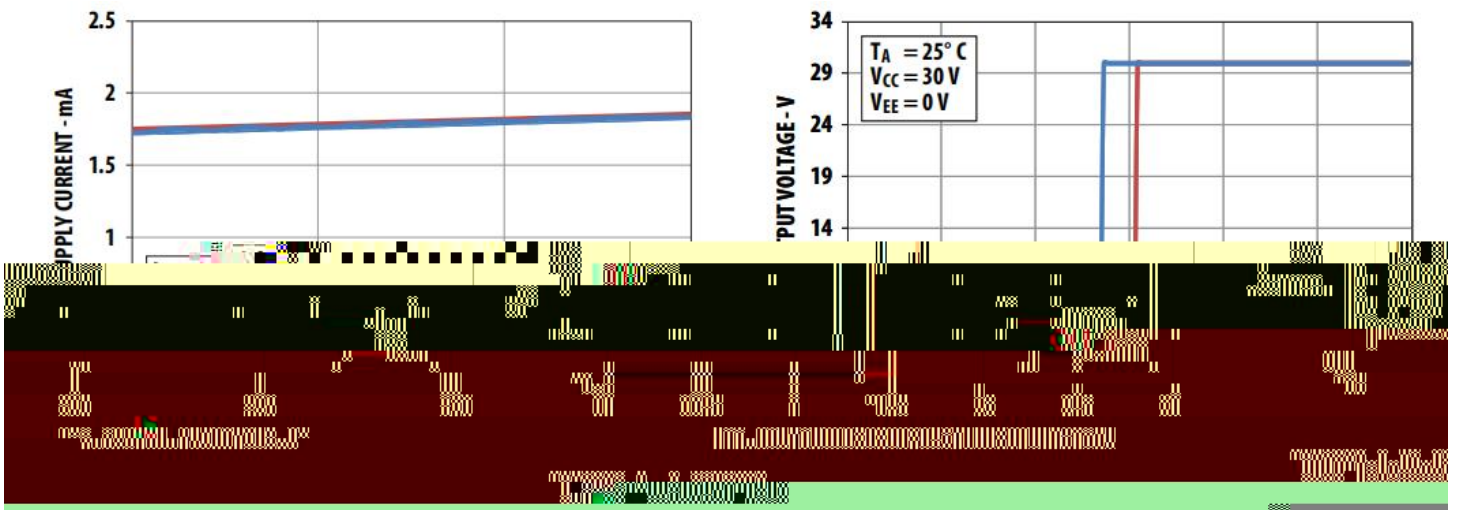


Figure 4. I_{CC} vs. temperature



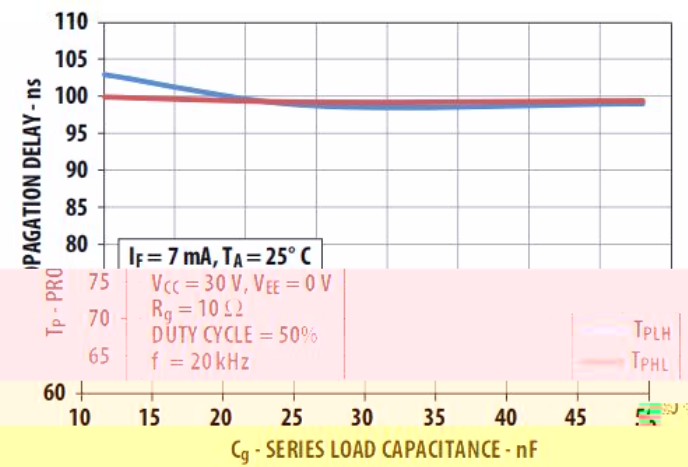
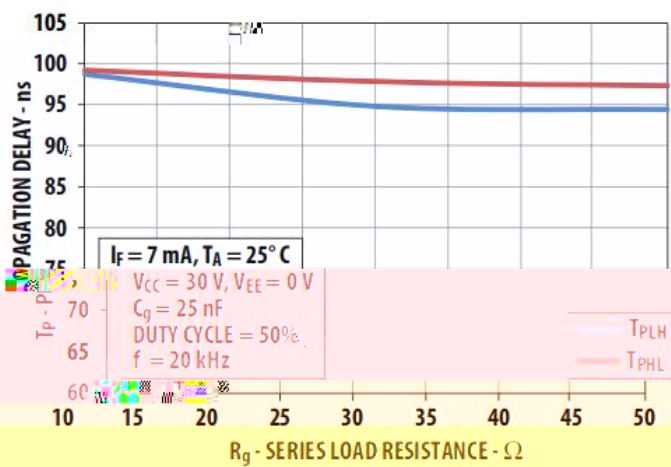
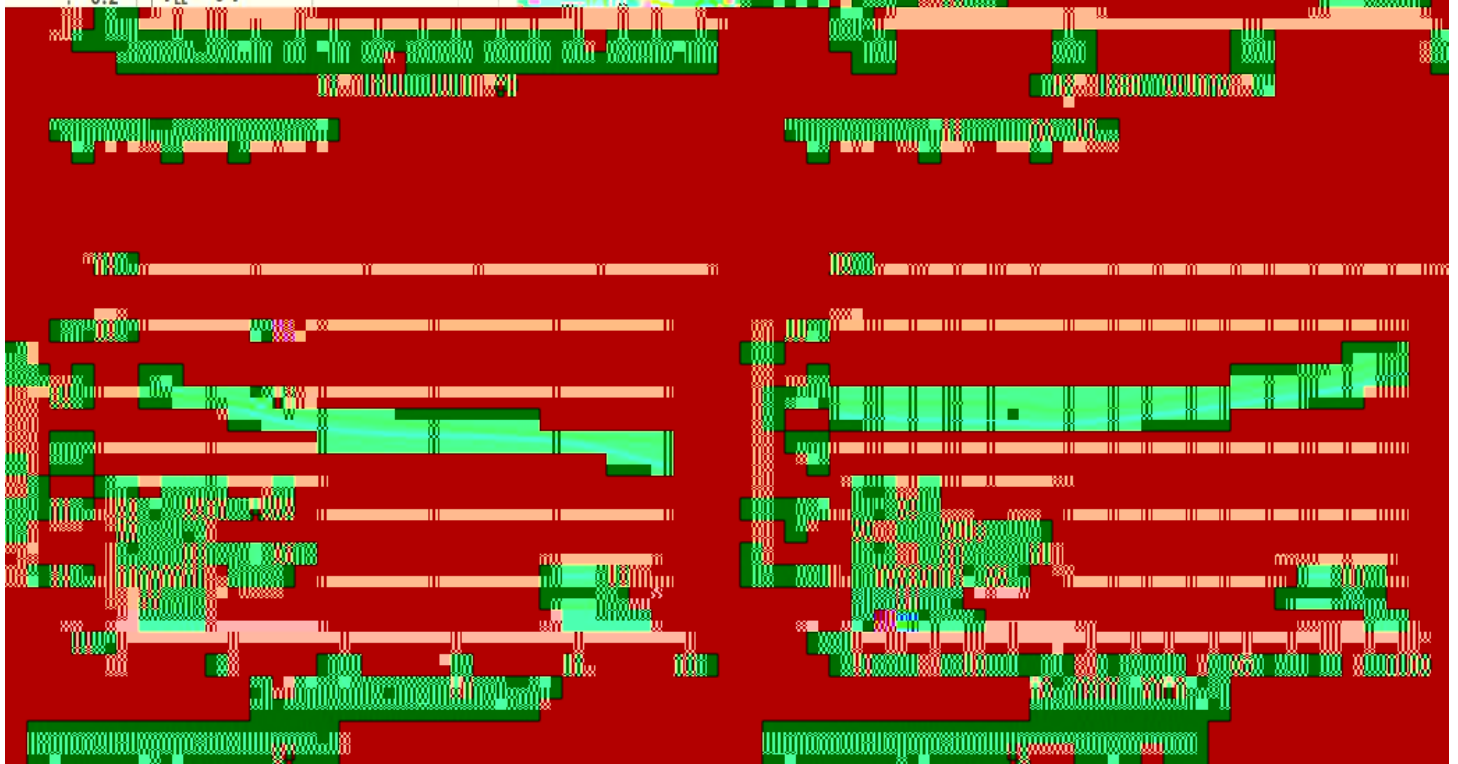
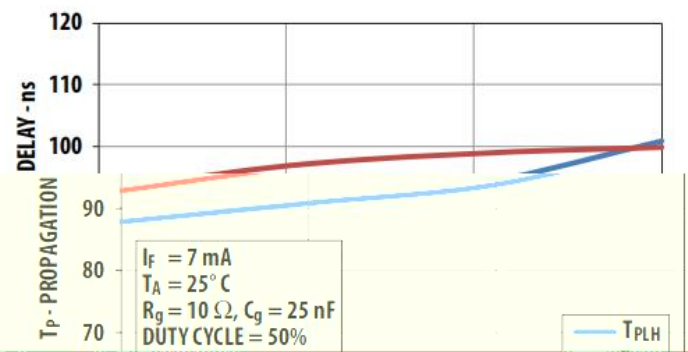


Figure 11. Propagation delay vs. R_g

Figure 12. Propagation delay vs. C_g

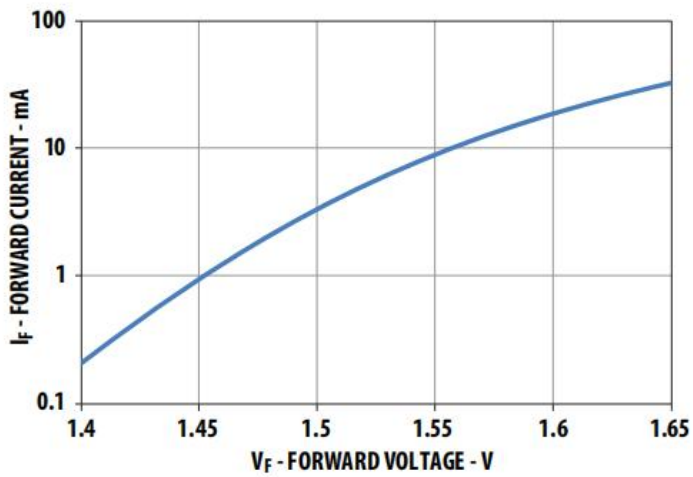


Figure 13. Input current vs. forward voltage

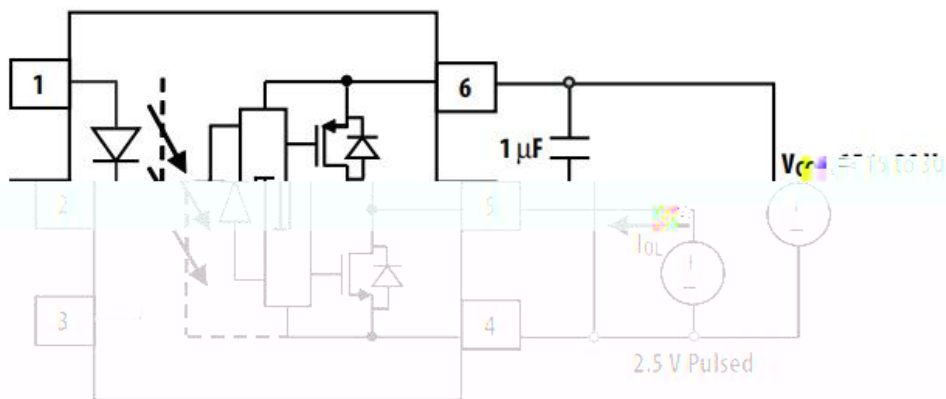
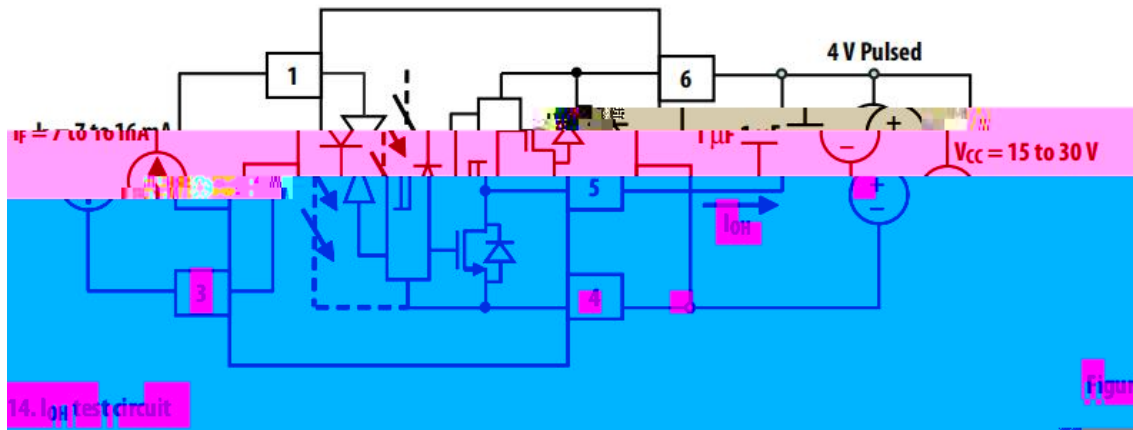


Figure 15. I_q test circuit

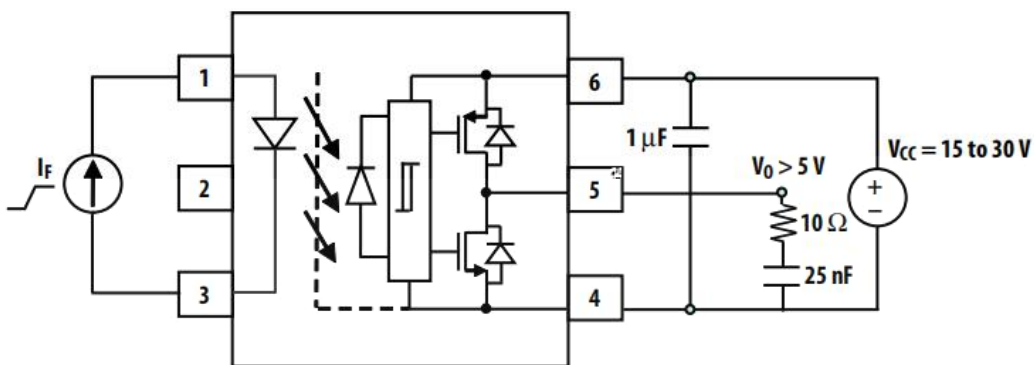
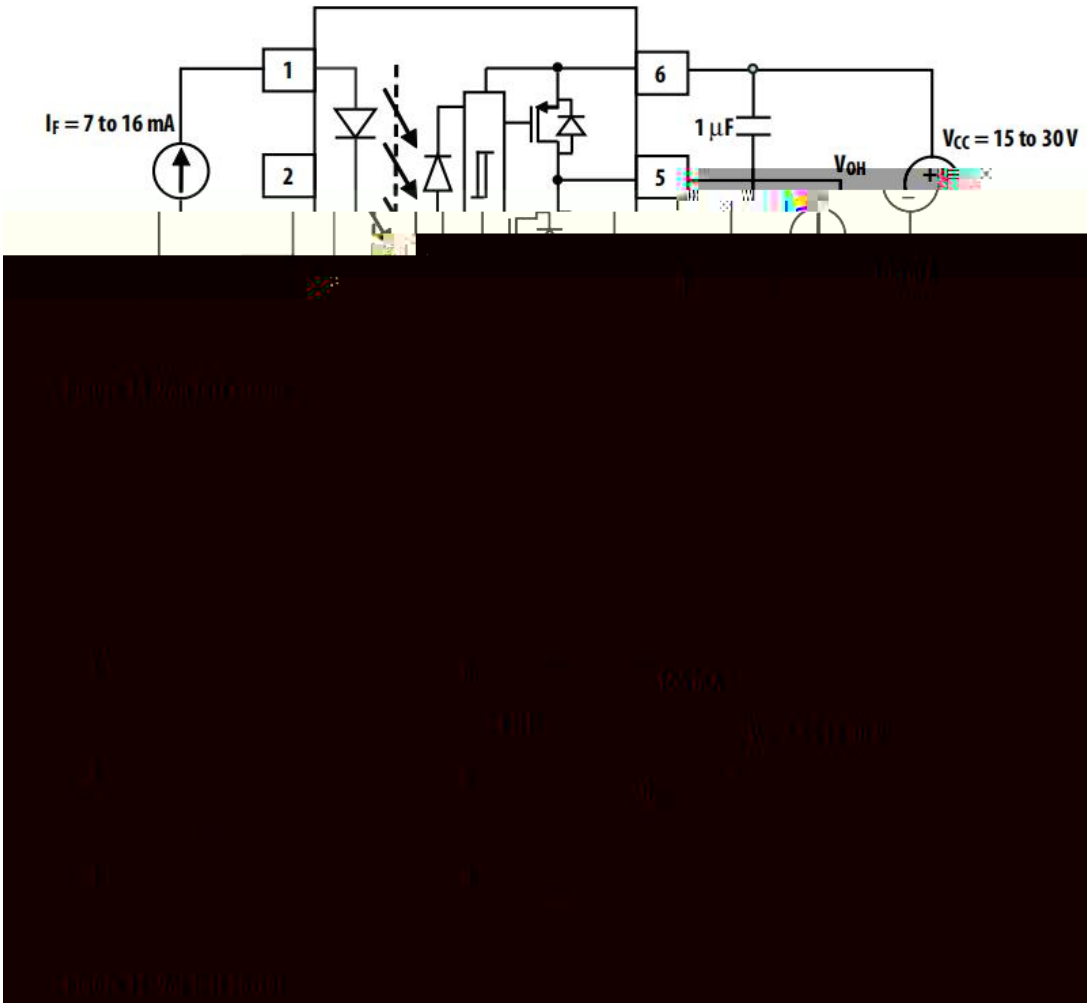


Figure 18. I_{FLH} test circuit

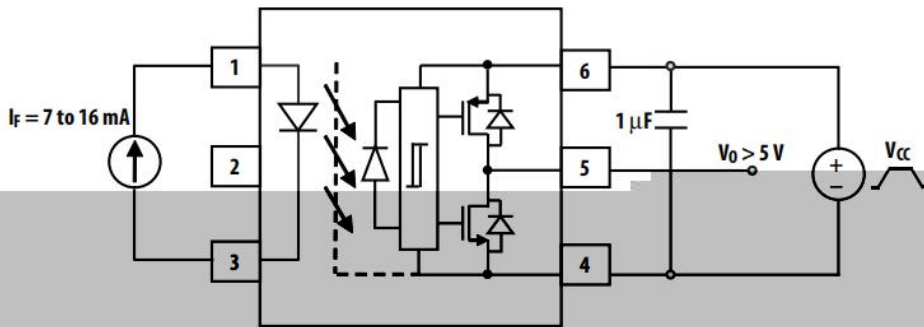


Figure 19. UVLO test circuit

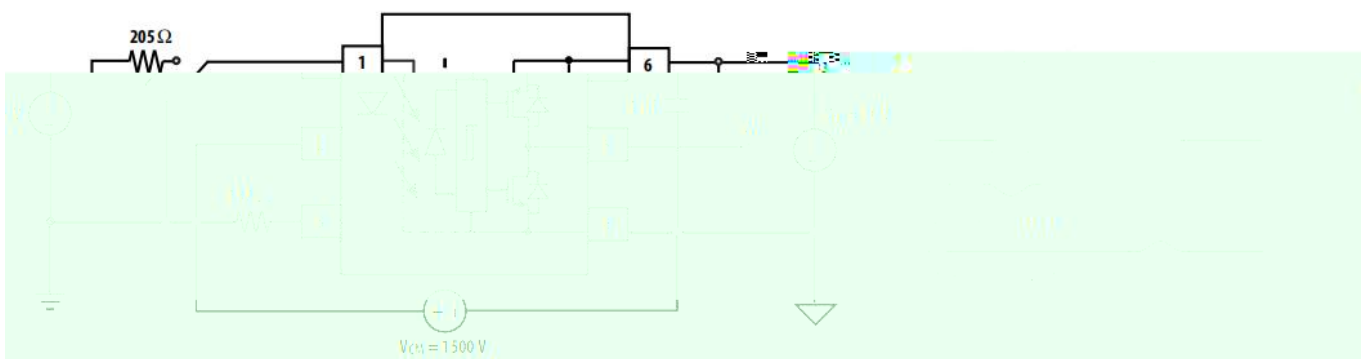
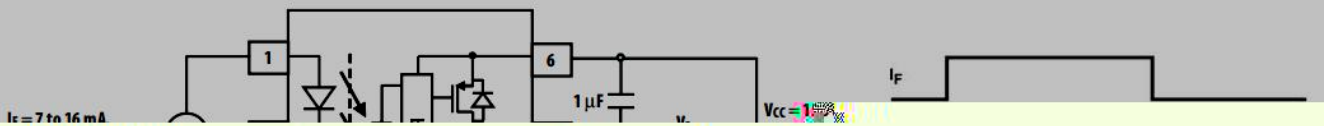


Figure 21. CMR test circuit with split resistors network and waveforms